

NAK1-BG55



GP 2784#4

**IN THE U.S. PATENT AND TRADEMARK OFFICE**

In Re Application of:

Hiroyuki Waki et al.

Serial No.: 09/288,263

Filed: April 4, 1999

For: **HIGH SPEED VIRTUAL MACHINE  
AND COMPILER**

Examiner:

Group Art Unit: 2784

Irvine, CA 92614

July 14, 1999

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**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

In an attempt to fully comply with the duty of disclosure set forth in 37 C.F.R. § 1.56 and in conformance with 37 C.F.R. §§ 1.97 and 1.98, applicants wish to bring to the attention of the U.S. Patent Office the following reference:

"Stack Caching for Interpreters," by M. A. Ertl,  
© 1995 ACM

A copy of this reference and a form PTO 1449 are attached.

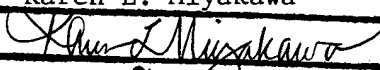
If the Examiner believes that a telephone conference would help further the prosecution of this case, he is respectfully requested to contact the undersigned attorney at the listed telephone number.

Respectfully submitted,

PRICE, GESS & UBELL

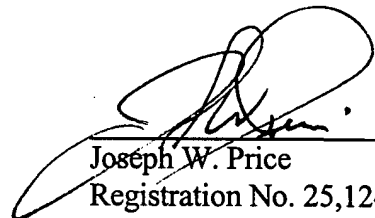
I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington DC 20231

on July 14, 1999  
Karen L. Miyakawa

  
Signature

July 14, 1999

Date

  
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